DECLARATION AND POWER OF ATTORNEY FOR PATENT APPLICATION

as the below named inventor, I hereby declare that:

My residence, post office address and citizenship are as stated below, next to my name.

I believe I am the original, first and sole inventor of the subject matter which is claimed and for which a United States Letter Patent is sought on the invention entitled "METHOD OF FABRICATING VERTICAL INTEGRATED CIRCUITS", the specification of which was filed on November 19, 2003 as United States Application No. 10/717,219.

I hereby state that I have reviewed and understand the contents of the above-identified Specification, including the Claims to Invention.

I hereby acknowledge the duty to disclose information, which is material to patentability as defined in 37 CFR 1.56. I hereby additionally acknowledge the duty to disclose all information known to be material to patentability which became available between the filing date of the prior application and the national or PCT international filing date of the continuation-in-part application as defined in 37 CFR 1.56.

Under 35 USC Section 119(e), I hereby claim the benefit of the earlier filing dates of United States Provisional Patent Application Serial No. 60/428,125 filed on November 20, 2002 entitled "Three Dimensional Device Assembly and Production Methods Thereof."

Under 35 USC Section 120, I hereby claim the benefit of the earlier filing dates of United States Patent Application Serial No. 09/950,909 filed on September 12, 2001 entitled "Thin Films and Production Methods Thereof."

I hereby declare that all statements made herein of my own knowledge are true, and that all statements made herein on information and belief are believed to be true, and further that these statements were made with the knowledge that willful false statements and the like are punishable by fine or imprisonment, or both, under 18 U.S.C. 1001, and that willful false statements may jeopardize the validity of the application or any patent issued thereon.

POWER OF ATTORNEY:

As a named inventor, I hereby appoint Ralph J. Crispino, Reg. No. 46,144 and Bosco Kim, Reg. No. 41,896, to prosecute this application and transact all business In the Patent and Trademark Office connected herewith.

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DECLARATION AND POWER OF ATTORNEY FOR PATENT APPLICATION For: METHOD OF FABRICATING VERTICAL INTEGRATED CIRCUITS Filed: November 19, 2003
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